

L Number	Hits	Search Text	DB	Time stamp
1	8	"5784593"	USPAT; US-PGPUB; IBM_TDB	2002/08/18 12:07
-	461462	model\$3 or simulat\$4 or emulat\$4	USPAT; US-PGPUB; IBM_TDB	2002/08/12 17:47
-	189475	boundary or 'fanin cone'	USPAT; US-PGPUB; IBM_TDB	2002/08/12 17:47
-	15	'fanin cone'	USPAT; US-PGPUB; IBM_TDB	2002/08/12 17:57
-	25	"5469366"	USPAT; US-PGPUB; IBM_TDB	2002/08/12 18:01
-	12952	(model\$3 or simulat\$4 or emulat\$4) and (boundary or 'fanin cone') and logic	USPAT; US-PGPUB; IBM_TDB	2002/08/12 18:23
-	1034522	partit\$4 or division or cell	USPAT; US-PGPUB; IBM_TDB	2002/08/12 18:24
-	9918	((model\$3 or simulat\$4 or emulat\$4) and (boundary or 'fanin cone') and logic) and (partit\$4 or division or cell)	USPAT; US-PGPUB; IBM_TDB	2002/08/12 18:24
-	3229	((model\$3 or simulat\$4 or emulat\$4) and (boundary or 'fanin cone') and logic) and (partit\$4 or division or cell)) and latch	USPAT; US-PGPUB; IBM_TDB	2002/08/12 18:25
-	1228	((model\$3 or simulat\$4 or emulat\$4) and (boundary or 'fanin cone') and logic) and (partit\$4 or division or cell)) and latch and node	USPAT; US-PGPUB; IBM_TDB	2002/08/12 18:25
-	376974	semiconductor or 'integrated circuit'	USPAT; US-PGPUB; IBM_TDB	2002/08/12 18:26
-	552944	semiconductor or 'integrated circuit' or processor	USPAT; US-PGPUB; IBM_TDB	2002/08/12 18:27
-	996	(((((model\$3 or simulat\$4 or emulat\$4) and (boundary or 'fanin cone') and logic) and (partit\$4 or division or cell)) and latch) and node) and (semiconductor or 'integrated circuit'))	USPAT; US-PGPUB; IBM_TDB	2002/08/12 18:26
-	1188	(((((model\$3 or simulat\$4 or emulat\$4) and (boundary or 'fanin cone') and logic) and (partit\$4 or division or cell)) and latch) and node) and (semiconductor or 'integrated circuit' or processor))	USPAT; US-PGPUB; IBM_TDB	2002/08/12 18:28
-	680	(((((model\$3 or simulat\$4 or emulat\$4) and (boundary or 'fanin cone') and logic) and (partit\$4 or division or cell)) and latch) and node) and (semiconductor or 'integrated circuit' or processor)) and map	USPAT; US-PGPUB; IBM_TDB	2002/08/12 18:28
-	839	(((((model\$3 or simulat\$4 or emulat\$4) and (boundary or 'fanin cone') and logic) and (partit\$4 or division or cell)) and latch) and node) and (semiconductor or 'integrated circuit' or processor)) and map\$4	USPAT; US-PGPUB; IBM_TDB	2002/08/12 18:30
-	419	(decompos\$4 or break\$3) and ((((((model\$3 or simulat\$4 or emulat\$4) and (boundary or 'fanin cone') and logic) and (partit\$4 or division or cell)) and latch) and node) and (semiconductor or 'integrated circuit' or processor)) and map\$4)	USPAT; US-PGPUB; IBM_TDB	2002/08/12 18:32

-	311	((decompos\$4 or break\$3) and ((((((model\$3 or simulat\$4 or emulat\$4) and (boundary or 'fanin cone') and logic) and (partit\$4 or division or cell)) and latch) and node) and (semiconductor or 'integrated circuit' or processor)) and map\$4)) and verif\$7	USPAT; US-PGPUB; IBM_TDB	2002/08/12 18:33
-	205	((decompos\$4 or break\$3) and ((((((model\$3 or simulat\$4 or emulat\$4) and (boundary or 'fanin cone') and logic) and (partit\$4 or division or cell)) and latch) and node) and (semiconductor or 'integrated circuit' or processor)) and map\$4)) and verif\$7) and (seed or cell)	USPAT; US-PGPUB; IBM_TDB	2002/08/12 18:36
-	21223	(decompos\$4 or break\$3 or segment\$3) same (semiconductor or IC or 'Integrated circuit')	USPAT; US-PGPUB; IBM_TDB	2002/08/18 09:47
-	3424	((decompos\$4 or break\$3 or segment\$3) same (semiconductor or IC or 'Integrated circuit')) and boundary	USPAT; US-PGPUB; IBM_TDB	2002/08/18 09:20
-	437	((decompos\$4 or break\$3 or segment\$3) same (semiconductor or IC or 'Integrated circuit')) and boundary) and latch	USPAT; US-PGPUB; IBM_TDB	2002/08/18 09:20
-	221	((decompos\$4 or break\$3 or segment\$3) same (semiconductor or IC or 'Integrated circuit')) and boundary) and latch) and (model\$3 or simulat\$4 or emulat\$4)	USPAT; US-PGPUB; IBM_TDB	2002/08/18 09:41
-	2671	(segment\$5 and circuit).ab.	USPAT; US-PGPUB; IBM_TDB	2002/08/18 09:42
-	374	((segment\$5 and circuit).ab.) and boundary	USPAT; US-PGPUB; IBM_TDB	2002/08/18 09:42
-	75	((segment\$5 and circuit).ab.) and boundary) and latch	USPAT; US-PGPUB; IBM_TDB	2002/08/18 09:43
-	881	(segment\$5 and (semiconductor or 'integrated circuit')).ab.	USPAT; US-PGPUB; IBM_TDB	2002/08/18 09:44
-	98	((segment\$5 and (semiconductor or 'integrated circuit')).ab.) and latch	USPAT; US-PGPUB; IBM_TDB	2002/08/18 09:44
-	19	((segment\$5 and (semiconductor or 'integrated circuit')).ab.) and latch) and boundary	USPAT; US-PGPUB; IBM_TDB	2002/08/18 09:44
-	1890	((decompos\$4 or break\$3 or segment\$3) same (semiconductor or IC or 'Integrated circuit')).ab.	USPAT; US-PGPUB; IBM_TDB	2002/08/18 09:47
-	145	((decompos\$4 or break\$3 or segment\$3) same (semiconductor or IC or 'Integrated circuit')).ab.) and latch	USPAT; US-PGPUB; IBM_TDB	2002/08/18 09:47
-	103	((decompos\$4 or break\$3 or segment\$3) same (semiconductor or IC or 'Integrated circuit')).ab.) and latch) and transistor	USPAT; US-PGPUB; IBM_TDB	2002/08/18 09:47
-	83	((decompos\$4 or break\$3 or segment\$3) same (semiconductor or IC or 'Integrated circuit')).ab.) and latch) and transistor) and (boundary or edge or cell)	USPAT; US-PGPUB; IBM_TDB	2002/08/18 09:52
-	0	(decompos\$4 and boundary and latch).ab.	USPAT; US-PGPUB; IBM_TDB	2002/08/18 09:53
-	74	(boundary and latch).ab.	USPAT; US-PGPUB; IBM_TDB	2002/08/18 10:01
-	0	((boundary and latch).ab.) and decompos\$4	USPAT; US-PGPUB; IBM_TDB	2002/08/18 09:55
-	0	(decompos\$5 and latch).ab.	USPAT; US-PGPUB; IBM_TDB	2002/08/18 12:07